

Docket No.: 57454-280

PATENT

1c872 U.S. PTO
09/986583
11/09/01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Tsukasa OOISHI

Serial No.:

Filed: November 09, 2001

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE CAPABLE OF SELF-TESTING
INTERNAL POWER SUPPLY CURRENTS PROVIDED TO INTERNAL CIRCUITS
INTEGRATED ON CHIP

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Group Art Unit:

Examiner:

3 / IDS
E. Willis
3-8-02

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, DC 20231

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.


This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits.

Serial No.:

No certification or fee is required.

Respectfully submitted,

MCDERMOTT, WILL & EMERY


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